Publications

Timm Bostelmann

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[1] Tobias Thiemann, Timm Bostelmann, and Sergei Sawitzki. Improving the gradient descent based FPGA-placement algorithm. In *The Thirteenth International Conference on Advances in Circuits, Electronics and Micro-electronics (CENICS)*, pages 12–24, November 2020.

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[3] Timm Bostelmann, Tobias Thiemann, and Sergei Sawitzki. Accelerating FPGA-placement with a gradient descent based algorithm. In *The Twelfth International Conference on Advances in Circuits, Electronics and Micro-electronics (CENICS)*, pages 13–18, October 2019.

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[10] Timm Bostelmann and Sergei Sawitzki. A heterogeneous architecture template for application domain specific reconfigurable logic. In *2015 Austrian Workshop on Microelectronics (Austrochip)*, pages 9–14, September 2015. IEEE.

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[12] Timm Bostelmann and Sergei Sawitzki. A conceptual toolchain for an application domain specific reconfigurable logic architecture. In *International Conference on Reconfigurable Computing and FPGAs (ReConFig)*, pages 1–4, December 2014. IEEE.

[13] Hanno Sternberg, Timm Bostelmann, and Sergei Sawitzki. CustArD - a custom architecture design tool. In *International Conference on Reconfigurable Computing and FPGAs (ReConFig)*, December 2014. IEEE.

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